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Docket No.: R2184.0247/P247  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Keiichi Yoshioka

Application No.: 10/633,681

Confirmation No.: 6878

Filed: August 5, 2003

Art Unit: 2815

For: SEMICONDUCTOR INTEGRATED  
CIRCUIT DEVICE AND FABRICATION  
METHOD THEREOF

Examiner: E. J. Wojciechowicz

**AMENDMENT IN RESPONSE TO NON-FINAL OFFICE ACTION**

**MS Amendment**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

**INTRODUCTORY COMMENTS**

In response to the Office Action dated December 31, 2007, please amend the above-identified U.S. patent application as follows:

**Amendments to the Claims** begin on page 2 of this paper.

**Remarks** begin on page 7 of this paper.